

VS³: SMT Solvers for Program Verification

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Abstract. We present VS³, a tool that automatically verifies complex properties of programs and infers maximally weak preconditions and maximally strong postconditions by leveraging the power of SMT solvers. VS³ discovers program invariants with arbitrary, but prespecified, quantification and logical structure. The user supplies VS³ with a set of predicates and invariant templates. VS³ automatically finds instantiations of the unknowns in the templates as subsets of the predicate set. We have used VS³ to automatically verify $\forall\exists$ properties of programs and to infer worst case upper bounds and preconditions for functional correctness.

1 Introduction

There are two major hurdles in the widespread adoption of current verification tools for program analysis. The first is their inability to express invariants with the detailed logical structure (specifically, quantifiers and disjunctions) that is required for almost all non-trivial programs. The second is the annotation burden and effort required on the part of the programmer in reasoning formally about inductive invariants. The sophisticated invariants required for most non-trivial programs are hard for the programmer to come up with and impose a significant annotation burden.

In this tools paper, we describe VS³ (Verification and Synthesis using SM T Solvers), a tool that partly addresses the above mentioned limitations by leveraging the engineering advances made in constraint solving in the past decade. VS³ takes as input a set of invariant templates and a set of candidate predicates to populate the templates' unknowns. VS³ then infers required program invariants and, optionally, required maximally weak preconditions. By allowing the invariants to have an arbitrary, but prespecified, logical structure, VS³ can verify sophisticated (e.g., \forall and $\forall\exists$) properties of programs. Additionally, by automatically inferring maximally weak preconditions, VS³ reduces the burden on the programmer, both in coming up with difficult preconditions and in specifying them alongside the code. In the future, we plan to augment VS³ with template inference and abstraction refinement to build a powerful system for software verification with minimal user input.

We have used VS³ with great success to verify a wide array of difficult program properties, such as the full functional correctness of standard implementations of all major sorting algorithms [1], and have additionally used it to infer difficult preconditions for functional correctness. VS³ interfaces with off-the-shelf

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<pre> (a) BinarySearch(Array A, int e, int n) 1 low := 0; high := n - 1; 2 while (low ≤ high) 3 mid := ⌈(low + high)/2⌉; 4 if (A[mid] < e) 5 low := mid + 1; 6 else if (A[mid] > e) 7 high := mid - 1; 8 else return true; 9 Assert (∀j : (0 ≤ j < n) ⇒ A[j] ≠ e) 10 return false; </pre> <p>User Input: <i>Template:</i> $v_1 \wedge (\forall j : v_2 \Rightarrow v_3)$ $\wedge (\forall j : v_4 \Rightarrow v_5) \wedge (\forall j : v_6 \Rightarrow v_7)$</p> <p><i>Predicate Set:</i> AllPreds($\{j, n, low, high\}, \{0\}, \{\leq, <\}$) \cup AllPreds($\{A[t] \mid t \in \{j, j \pm 1\}\} \cup \{e\}, \{0\},$ $\{\leq, \neq\}$)</p> <p>Tool Output: “Precondition required”: <i>Precondition:</i> $\forall j : (0 \leq j < n) \Rightarrow A[j] \leq A[j + 1]$ <i>Loop Invariant:</i> $0 \leq low \wedge high < n$ $\forall j : (low \leq j \leq high) \Rightarrow A[j] \leq A[j + 1]$ $\forall j : (0 \leq j < low) \Rightarrow A[j] \neq e$ $\forall j : (high < j < n) \Rightarrow A[j] \neq e$</p>	<pre> (b) SetInclusion(Array A, int n, Array B, int m) 1 for (i = 0; i < n; i++) 2 exists := false; 3 for (j = 0; j < m; j++) 4 if (A[i] = B[j]) 5 exists := true; break; 6 if (¬exists) return false; 7 Assert (∀y∃x : (0 ≤ y < n) ⇒ (A[y] = B[x] ∧ 0 ≤ x < m)) 9 return true; </pre> <p>User Input: <i>Template:</i> $v_1 \wedge (\forall y \exists x : v_2 \Rightarrow v_3)$ $\wedge (\forall y \exists x : v_4 \Rightarrow v_5)$</p> <p><i>Predicate Set:</i> AllPreds($\{x, y, i, j, m\}, \{0\}, \{\leq, <\}$) \cup AllPreds($\{exists\}, \{true, false\}, \{=\}$) \cup AllPreds($\{A[t], B[t] \mid t \in \{x, y\}\}, \{0\}, \{=\}$)</p> <p>Tool Output: “Assertion valid”: <i>Outer loop invariant:</i> $\forall y \exists x : 0 \leq y < i \Rightarrow (A[y] = B[x] \wedge 0 \leq x < m)$ <i>Inner loop invariant:</i> $j \geq 0$ $\forall y \exists x : 0 \leq y < i \Rightarrow (A[y] = B[x] \wedge 0 \leq x < m)$ $\forall y \exists x : (y = i \wedge exists = true)$ $\Rightarrow (A[y] = B[x] \wedge 0 \leq x < m)$</p>
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Fig. 1. (a) VS³ computes the maximally weak precondition for correctness of binary search (b) VS³ computes the $\forall\exists$ invariants required to prove the correctness of a program that checks set inclusion. (**AllPreds**(Z, C, R) denotes the set of predicates $\{z \text{ op } z' \mid z, z' \in Z \cup C, \text{op} \in R\}$.)

SMT solvers for its core reasoning and provides a rich source for SMT benchmark instances from the domain of verification and property inference of programs.

1.1 Motivating Examples and Tool Usage

We now illustrate the power of VS³ by considering two motivating examples.

Precondition Inference. Consider the program shown in Fig. 1(a), which implements a binary search for the element e in an array A . The functional specification of the program is given as the assertion³ on Line 9, which essentially states that if the procedure returns false, then A indeed does not contain e .

³ VS³ allows the user to specify assertions and assumptions with arbitrary logical structure up to those expressible in the underlying SMT solver. Assumptions may be required to model expressions not handled by the solver. For instance, in the current system, the assignment on Line 3 is modeled as **Assume**($low \leq mid \leq high$).

For this function, VS³ automatically infers the maximally weak precondition for functional correctness, shown in Fig. 1(a), which is that the input array is sorted. VS³ also infers the loop invariant, also shown in Fig. 1(a), encoding the semantics of binary search (that the array elements between *low* and *high* are sorted and those outside do not equal *e*).

In automatic CUTPOINT mode, VS³ searches for inductive program invariants at loop headers. Alternatively, in some cases the invariants are simpler if inferred at specific locations, which should form a valid cutset such that each cycle in the CFG contains at least one location [2]. VS³ also supports a MANUAL mode for user-specified cutsets.

The user also specifies a *global* invariant template and a *global* predicate set, as shown. The template is used for invariants at each cutpoint and the predicate set specifies the candidate predicates for the unknowns in the template. In practice, the user starts by guessing simple templates and predicates. If the analysis fails then the user iteratively increases the sophistication of the templates and predicates until VS³ finds a solution.

In practice, the logical structure (quantification and boolean connectives) of the templates is easily derived from given program assertions and discovered by iterative guessing. For instance, for `BinarySearch` we first tried a template with one unquantified and one quantified conjunct, but VS³ failed to infer an instantiation. We then iteratively increased the number of quantified conjuncts until a solution was found. Also, typically we used a predicate set consisting of inequality relations between relevant program and bound variables. In our experience (over a large set of programs [1]), coming up with the templates and predicate set is typically easy for the programmer.

Verification using invariants with arbitrary logical structure. Consider the program shown in Fig. 1(b), which checks whether all elements of *A* are contained in *B*. The loop invariant required contains $\forall\exists$ quantification, which VS³ infers. We do not know of any other tool that can automatically discover such invariants. Note how the conjuncts in the invariant template in this case follow the schematic of the given assertion and therefore are $\forall\exists$ -quantified. As before, we discovered the appropriate number of conjuncts by iterative guessing.

2 Tool Architecture

VS³ uses Microsoft’s Phoenix compiler framework [3] as a front end parser for ANSI-C programs and Z3 [4] to solve the SMT queries. Our implementation is approximately 15K non-blank, non-comment lines of C# code.

The tool architecture is shown in Fig. 2. VS³ uses Phoenix to generate the control flow graph (CFG) of the program. The CFG is then split into simple paths using a cutset (either generated automatically with a cutpoint at each loop header or specified by the user). The tool then generate a verification condition (VC) corresponding to each simple path. For fixed-point computation the tool provides two alternatives:

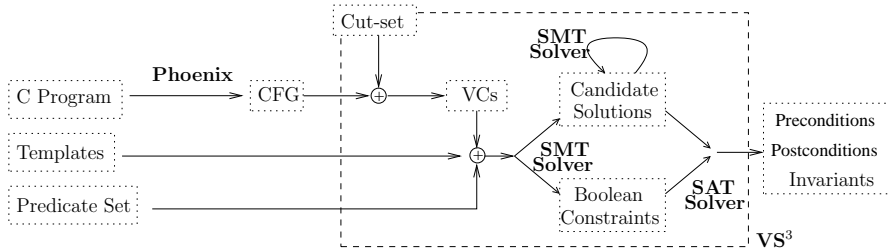


Fig. 2. The VS³ tool. In addition to the ANSI-C program, the user provides the templates, the predicate sets, and optionally, a cutset. The tool provides the option of choosing between an iterative and a constraint-based fixed-point computation.

Iterative Fixed-Point [1]. The iterative scheme performs a variant of a standard dataflow analysis. It maintains a set of candidate solutions, and by using the SMT solver to compute the best transformer it iteratively improves the candidates until a solution is found. See [1] for details.

Constraint-based Fixed-Point [5, 1]. In the constraint-based scheme, a predicate p at location l is identified by boolean indicator variables $b_{p,l}$. For verification condition vc , VS³ generates the minimal set of constraints over the indicator variables that ensures that vc is satisfiable. These constraints are accumulated and solved using a SAT solver that yields a fixed-point solution. See [1] for details.

2.1 Solver Interface

Compensating for current limitations of SMT solvers The generic primitives provided by SMT solvers are expressive but lacking in some aspects that are needed for our application.⁴ We augment the solver by providing a wrapper interface that preprocesses the SMT queries and adds hints for the solver.

Patterns for quantifier instantiation. The current state-of-art for reasoning over quantified facts uses the now commonly known technique of E-matching [6] for quantifier instantiation. E-matching uses *patterns* to match against ground terms. Because individual SMT queries in our system are over simple quantified terms, a simple heuristic to automatically generate patterns suffices: Given a quantified fact with bound variables \bar{k} and bound boolean term F , VS³ recursively parses F and returns valid patterns from F 's subterms. A subterm f is a valid pattern if it contains all the bound variables and at least one subterm of f does not contain all the variables. For example, for the fact $\forall k : k > 10 \Rightarrow A[k] < A[k+1]$, VS³ computes the set of patterns $\{\{k > 10\}, \{A[k]\}, \{A[k+1]\}\}$, and for $\forall k : k \geq 0 \wedge k < v \Rightarrow A[k] < min$, VS³ computes the set $\{\{k \geq 0\}, \{k < v\}, \{A[k] < min\}\}$. This simple heuristic is potentially expensive, but allows for automatic and, in practice, fast proofs or disproofs of the implication checks.

⁴ Our current implementation uses the Z3 solver, but the limitations we discuss here apply to all state-of-the-art solvers, as far as we know.

Saturating inductive facts. SMT solvers have difficulty instantiating relevant facts from inductive assumptions. For instance, in our experiments, we encountered assumptions of the form $k_n \geq k_0 \wedge \forall k : k \geq k_0 \Rightarrow A[k] \leq A[k+1]$ from which $A[k_0] \leq A[k_n+1]$ was needed for the proof. Z3 times out without finding a proof or disproof of whether $A[k_0] \leq A[k_n+1]$ follows from the assumption. Notice that the pattern $k \geq k_0$ will only allow the prover to instantiate $A[k_n] \leq A[k_n+1]$ from the ground fact, which does not suffice to prove $A[k_0] \leq A[k_n+1]$.

VS³ therefore syntactically isolates inductive facts and saturates them. To do this, it pattern matches quantified assumptions such as the above (consisting of a base case in the antecedent and the induction step in the consequent of the implication) and asserts the quantified inductive result. For example, for the case above, the saturated fact consists of $\forall k_2, k_1 : k_2 \geq k_1 \geq k_0 \Rightarrow A[k_1] \leq A[k_2+1]$. Using the ground term $k_n \geq k_0$, the tool can now instantiate $A[k_0] \leq A[k_n+1]$. This heuristic allows the tool to deal with inductive reasoning for particular cases that arise in program analysis.

Explicit Skolemization for $\forall\exists$. Z3 v1.0 does not correctly instantiate global skolemization functions for existentials under a quantifier and so VS³ infers these functions from the program⁵. An approach that suffices for all our benchmark examples is to rename the skolemization functions at the endpoints of a verification condition and to insert axioms relating the two functions. VS³ syntactically infers the appropriate axioms over the skolem functions for the two specific cases that we describe below.

VS³ can infer appropriate skolemization functions for the two cases of the verification condition containing array updates and assumptions equating array values. Suppose in the quantified formulae at the beginning and end of a simple path, the skolemization functions are `sk1` and `sk1'`, respectively. For the case of array updates, suppose that locations $\{l_1, l_2, \dots, l_n\}$ are overwritten with values from locations $\{r_1, r_2, \dots, r_n\}$. Then the tool introduces two axioms. The first axiom states that the skolemization remains unchanged for locations that are not modified (Eq. 1), and the second axiom defines the (local) changes to the skolemization function for the array locations that are modified (Eq. 2):

$$\forall y : (\wedge_i (\text{sk1}(y) \neq r_i \wedge \text{sk1}(y) \neq l_i)) \Rightarrow \text{sk1}'(y) = \text{sk1}(y) \quad (1)$$

$$\bigwedge_i \forall y : \text{sk1}(y) = r_i \Rightarrow \text{sk1}'(y) = l_i \quad (2)$$

For the case of assumptions equating array values, VS³ asserts the corresponding facts on `sk1'`, e.g., if `Assume(A[i] = B[j])` occurs and `sk1'` indexes the array `B` then the tool adds the axiom `sk1'(i) = j`.

Axiomatic support for additional theories Some program verification tasks require support for non-standard expressions, e.g., reachability in linked-list or tree data structures. SMT solvers, and in particular Z3, support the addition of axioms to support these kind of predicates.

⁵ We are aware of work being pursued in the solving community that will eliminate this restriction. Therefore in the future we will not need to infer skolemization functions.

There are two steps towards the verification of such programs. First, VS³ defines the semantics of field accesses and updates on record datatypes using `sel` and `upd`. A field access $s \rightarrow f$, is encoded as `sel(f, s)` and an update $s \rightarrow f := e$ is encoded as `upd(f, s, e)`. Second, VS³ asserts a set of axioms to define the semantics of higher level predicates, such as reachability, in terms of the constructs that appear in the program. Let $x \rightsquigarrow y$ denote that y can be reached by following pointers starting at x . Then for the case of reasoning about singly linked lists connected through `next` fields, the tool augments the SMT solver with the following reachability axioms:

$\forall x . x \rightsquigarrow x$	Reflexivity
$\forall x, y, z . x \rightsquigarrow y \wedge y \rightsquigarrow z \Rightarrow x \rightsquigarrow z$	Transitivity
$\forall x . x \neq \perp \Rightarrow x \rightsquigarrow (x \rightarrow \mathbf{next})$	Step: Head
$\forall x, y . x \rightsquigarrow y \Rightarrow x = y \vee (x \rightarrow \mathbf{next}) \rightsquigarrow y$	Step: Tail
$\forall x . \perp \rightsquigarrow x \Rightarrow x = \perp$	End

Using these axioms the solver can, for instance, prove that $head \rightsquigarrow tail \wedge tail \rightsquigarrow n \wedge n \neq \perp \Rightarrow head \rightsquigarrow (n \rightarrow \mathbf{next})$.

3 Summary of Experiments and Conclusions

We have used VS³ to verify a wide variety of properties of programs manipulating arrays. We have verified the full correctness of standard implementations of five major sorting algorithms. Showing sortedness for these examples required \forall invariants, and showing permutation required $\forall\exists$ invariants (which have received little previous attention [7, 8]).

Additionally, we have successfully used VS³ for inferring maximally weak preconditions. We have derived maximally weak preconditions for functional correctness and for proving worst case upper bounds for programs, e.g., inferring the input that yields the worst case run of a sorting program. See [1] for details.

Our experiments with VS³ have demonstrated its promise for the verification and inference of difficult program properties.

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